

P2001,0304 - 10/694,591
Response to Office action 2/8/2006
Response submitted May 8, 2006

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Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended). A method for identifying a correct command entry address, the method which comprises:

providing each one of a plurality of short command words with a first start bit having a predetermined value;

providing each one of a plurality of long command words with the first start bit having the predetermined value and with a second start bit having a value different from the predetermined value of the first start bit; and

outputting a signal from a checking apparatus if a command entry address is not correct.

Claim 2 (original). The method according to claim 1, which further comprises:
outputting a signal from the checking apparatus if an intended entry point is the second start bit.

Claim 3 (original). The method according to claim 2, which further comprises:

providing each one of the plurality of long command words with a first command

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word half having a beginning and a second command word half having a beginning;

configuring the first start bit at the beginning of the first command word half; and

configuring the second start bit at the beginning of the second command word half.

Claim 4 (original). The method according to claim 3, which further comprises:
providing the value of the second start bit as an inverse of the value of the first start bit.

Claim 5 (original). The method according to claim 2, which further comprises:
providing the value of the second start bit as an inverse of the value of the first start bit.

Claim 6 (new). A method for identifying a correct command entry address, the method which comprises:

defining command words of mutually different lengths and including short command words and long command words, and each command word having a predetermined first start bit;

providing each long command word with a second start bit having a value different from the predetermined first start bit; and

checking the start bit with a checking apparatus, determining whether or not the start

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bit indicates a correct command entry address, and outputting a signal from the
checking apparatus if the command entry address is not correct.